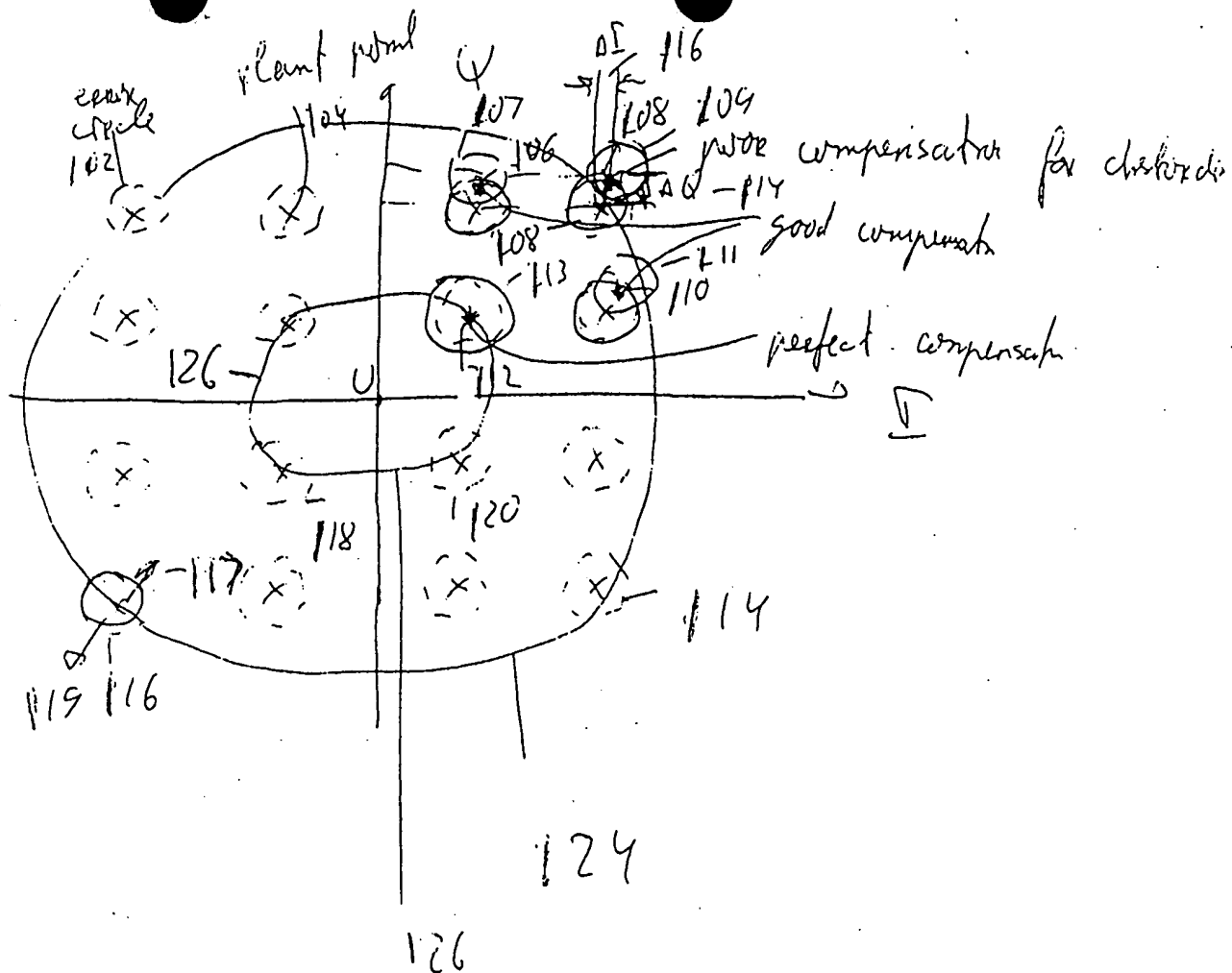


[illegible]

Verdula -105/Quark-195

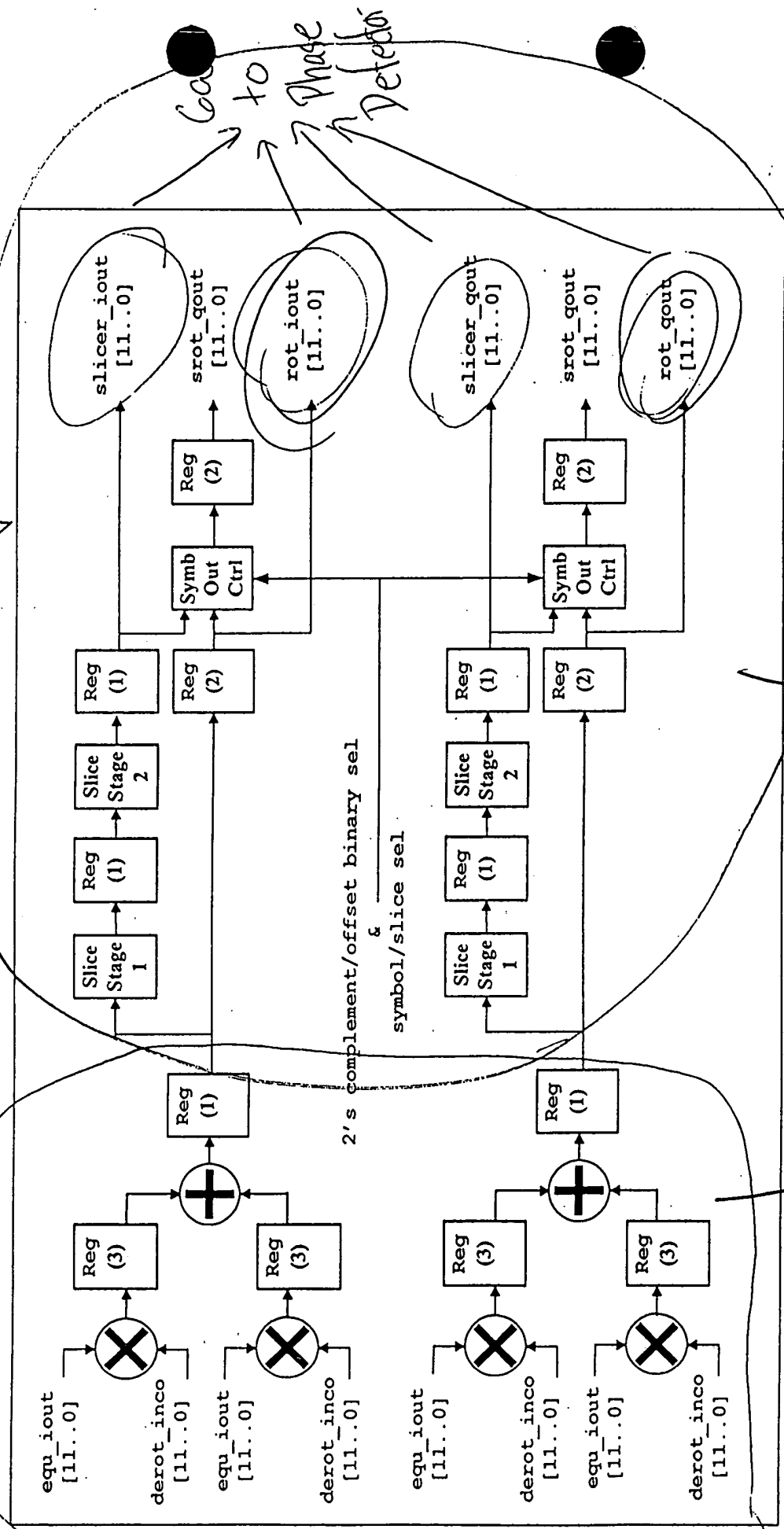
Wrote -105 / Oct-195

NO THIS IS NOT a complex multiplier used in the green PLC

Complex Multiplier

Complex

Derotator (H)

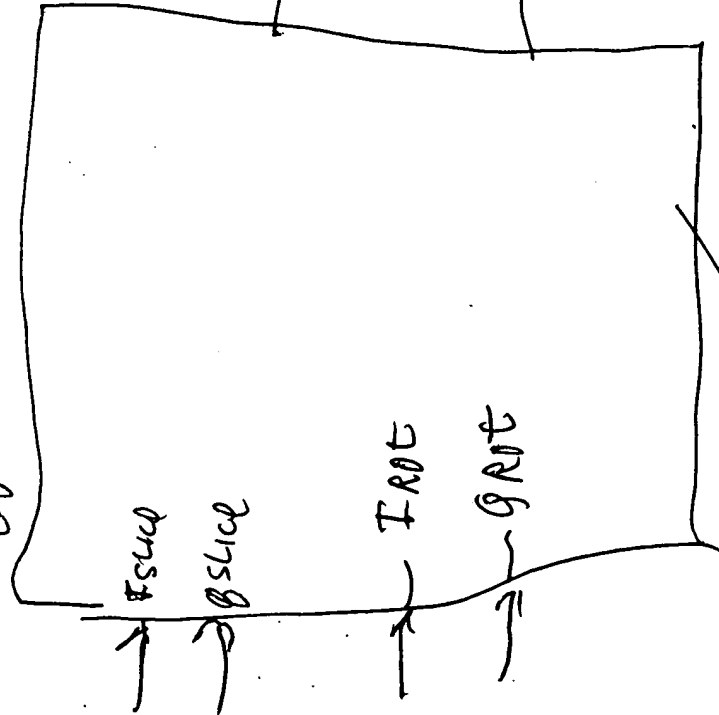


Carrier Derotation

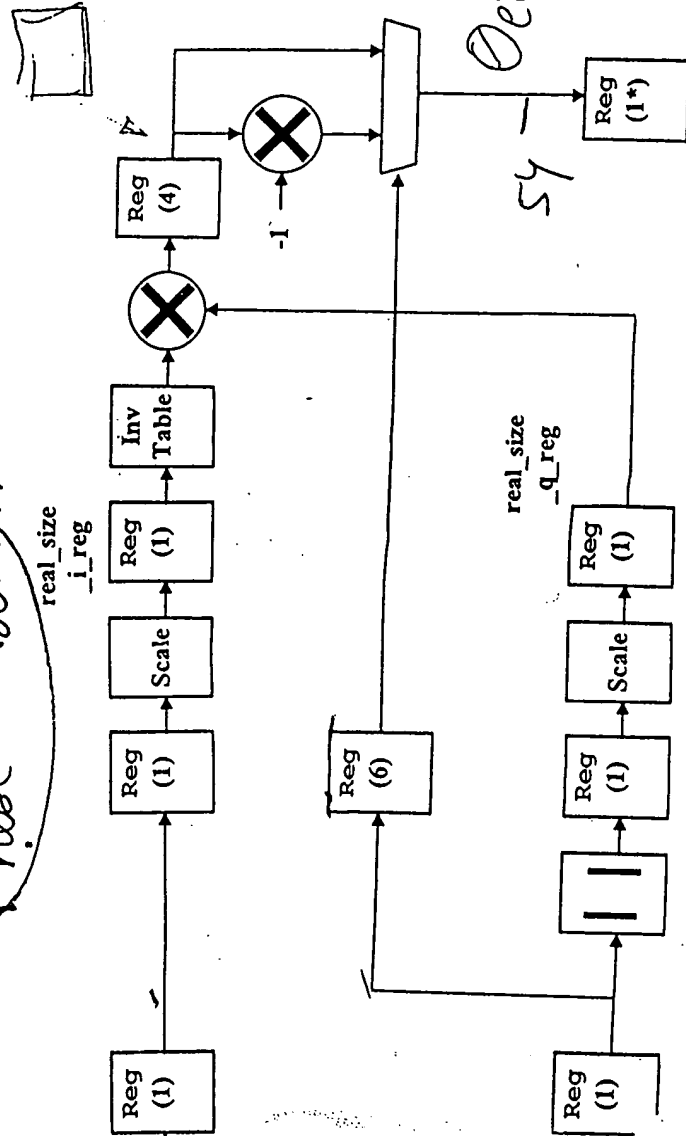
16

Fig. 3

complex multiplier MISSING (E)



Phase Detector



Clocked when
clk_valid = 0 and
clk_cnt = 1

Fig. 4

Wreldel-105/Gank-195-

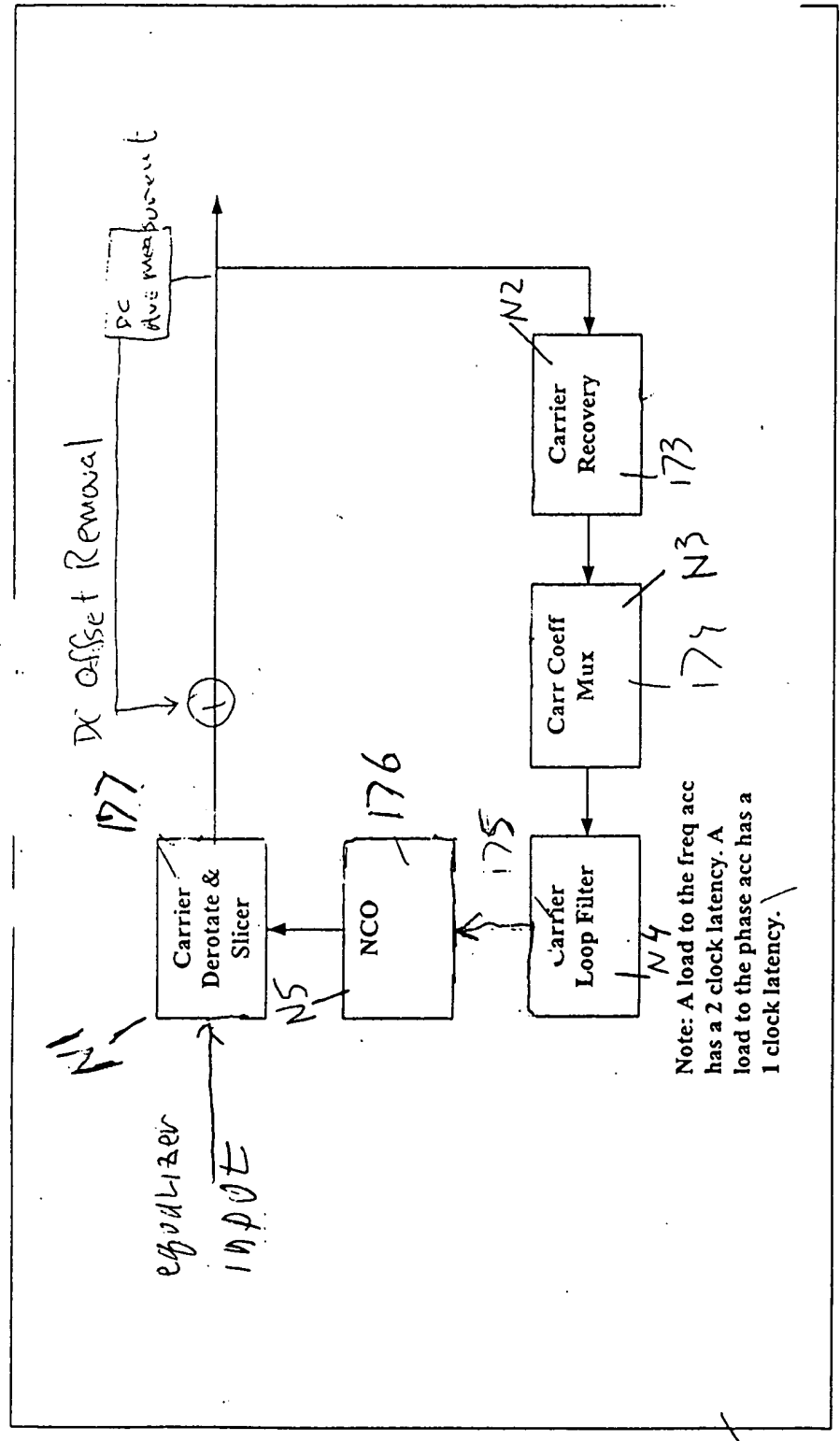


①



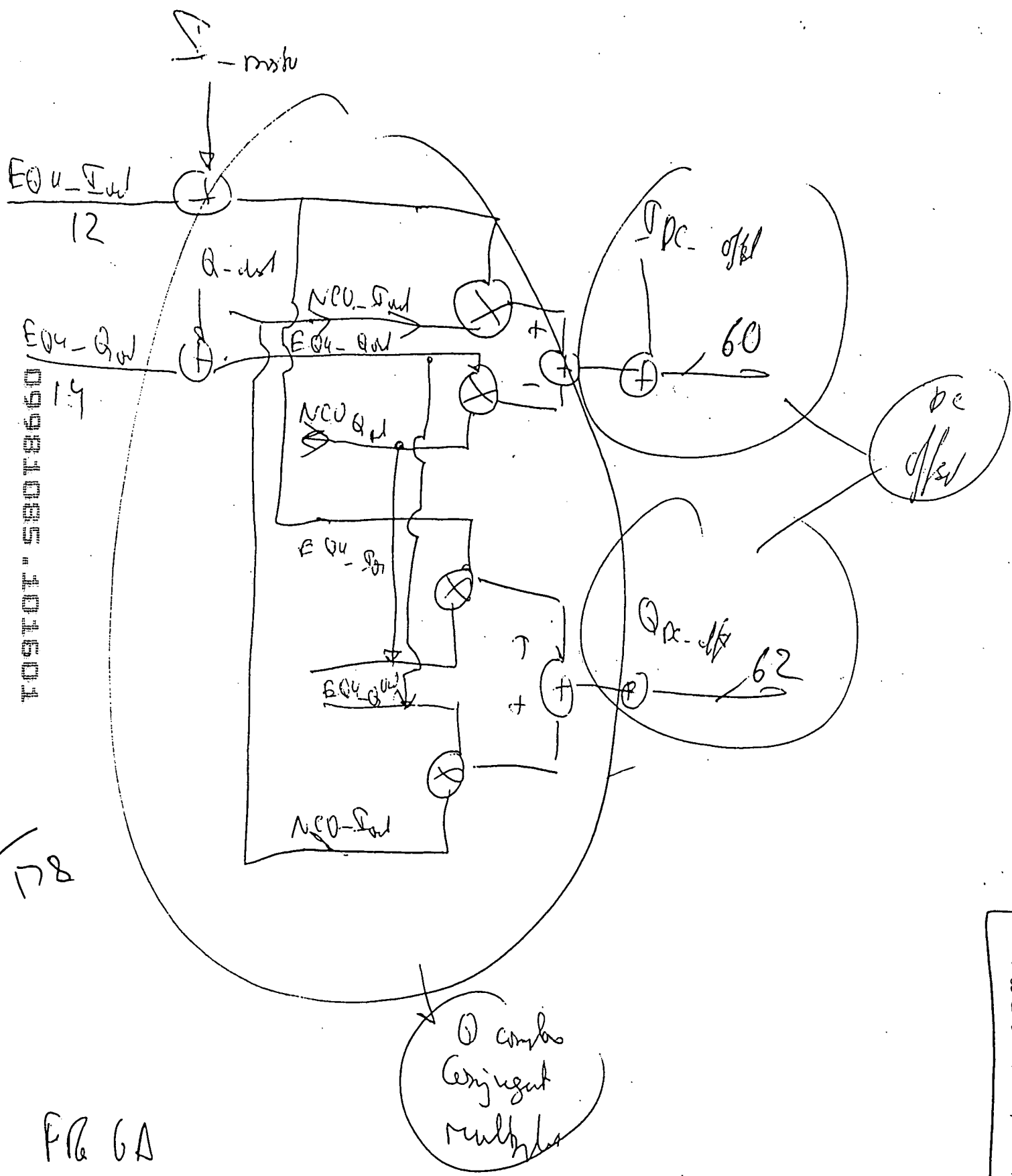
Worksheet - 105 / Part-195

TOTAL "SECTIONS"



Total Loop Latency

F050



09981085-101601

FIG 6A

Unclassified-105/Rev-195

DO correct effect of volatility over

Constellation Error Measurement

Const Avg Coeff (0.0 \rightarrow 1.0)

Const Error

192

removing noise effect

Experiment Averer.

722

0

if (Avg Coeff = 0)

if (avg CoEFF = 1)

2/5

output is held (input ignored)

OUTPUT = INPUT

OUTPUT IS EXPONENTIAL ~~2~~ AUG
OF INPUTS

23

FB. 6B

Insider
even now
8

Isymb, 10

①

12

28

Iskaf

100

3

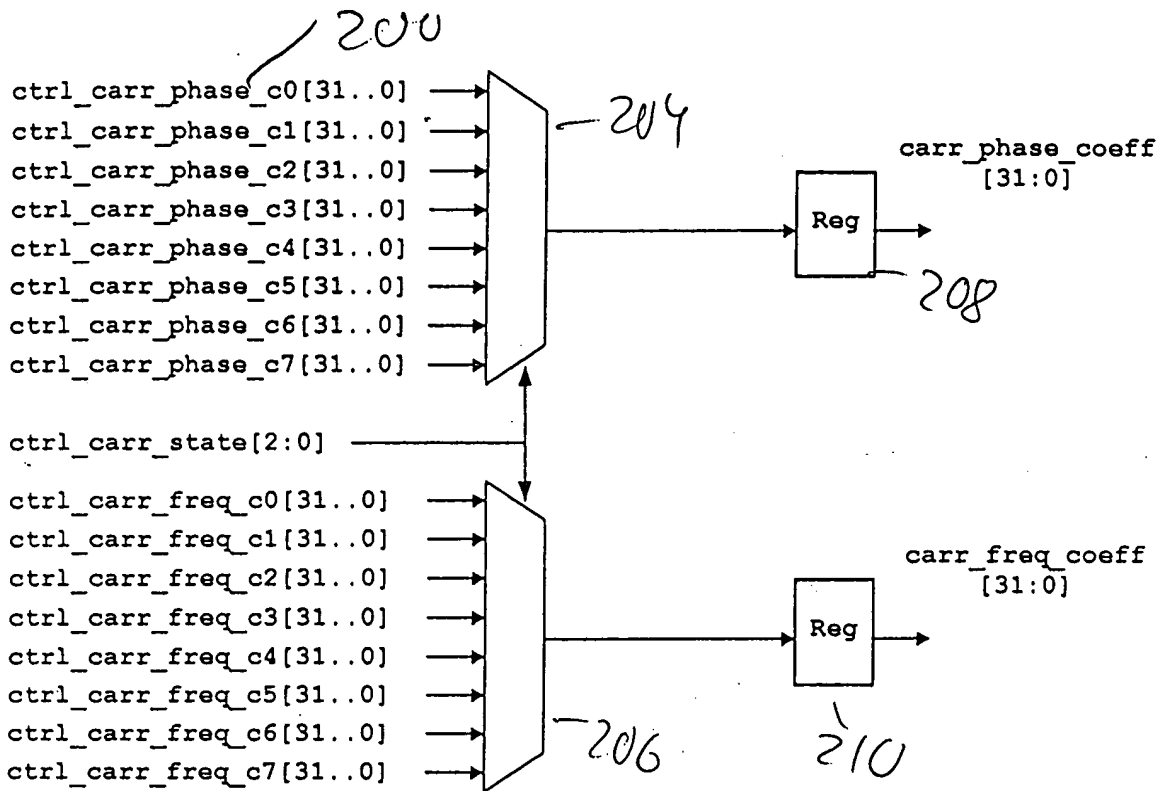
24/12

QslicE 26

(Amber of

Power Squared.

0981035-101500



AP. 7.

202

Wetland-105/Gar-145

09581085 "101601

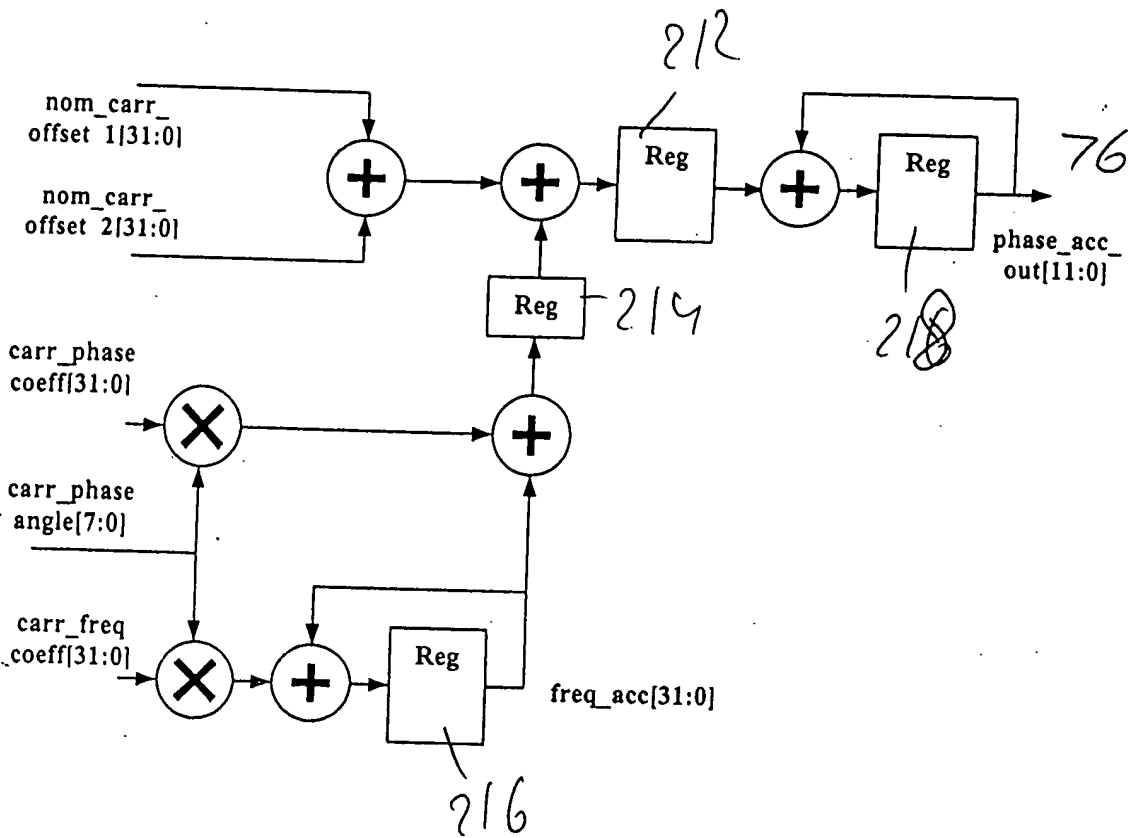


FIG 8. Carrier Loop Filter

start 232 250, 258

(A) Sampling a QAM signal received from a transmission channel.

(B) Recovering a symbol clock function from the sampled QAM signal.

(C) Applying the sampled QAM signal to the adaptive equalizer in order to obtain a QAM equalized signal in a Blind Equalization (BE) mode.

(D) Using a slicer to locate a nearest plant point for the QAM BE equalized signal for each recovered symbol clock.

(E) Using a phase detector to obtain an instantaneous inphase component and an instantaneous quadrature component of a phase error signal by comparing an inphase component and a quadrature component of the QAM BE equalized signal and an inphase and a quadrature component of the nearest plant point for each symbol clock.

Linear phase detector

(F) Using a complex conjugate multiplier to translate the inphase component and the quadrature component of the phase error signal into an instantaneous phase error vector for each symbol clock.

(G) Averaging the instantaneous phase error vector signal by using a carrier loop filter.

(H) Using a complex multiplier to insert an inverse of the averaged phase error vector signal into the QAM BE equalized signal to compensate for the carrier phase error.

(I) Repeating the steps (D-H) to close a carrier frequency loop.



230
FIG. 9

Writed -105/6 and -105

Selecting an initial set of PID coefficients by using the state machine to set the variable bandwidth of the carrier loop filter to be higher than a frequency uncertainty during a QAM signal acquisition state of the QAM demodulator.

262

Adjusting the initially selected set of PID coefficients by using the state machine in order to decrease the initially set bandwidth of the carrier loop filter in incremental stages to be less than the frequency uncertainty during a carrier tracking state of the QAM demodulator.

264

244

Step 6 - Normal Mode
F1610

update 105/6 at 195

266

(A) Starting with a first set of coefficients of the carrier frequency loop in the state machine corresponding to a normal set of input code words.



(B) Detecting a burst set of input code words.

268

NO

270 - 272 - 274

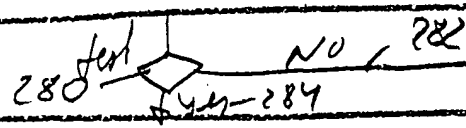
(C) Selecting a second set of coefficients of the carrier frequency loop in the state machine corresponding to the burst set of input code words for a predetermined amount of time to switch the QAM modem to a burst mode of operation.

276



(D) Switching the state machine back so that to set the carrier frequency loop includes the first set of coefficients after the burst mode is over.

278



(E) Repeating the steps (A-D).

286

244
Step 6 - Burst Mode

FIG. 11

W. H. H. - 105 / 100 - 195

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